

AMENDMENTS TO THE CLAIMS

The following listing of claims replaces all previous listings and versions of claims in this application.

1. (Currently Amended) An electric motor linear speed controller, comprising:
 - a digital to analog converter means for converting an 8-bit digital signal to an analog voltage for setting voltage across a motor;
 - a digital state machine means for converting the duty cycle of an input signal for output to the digital to analog converter means; and
 - a closed loop feedback loop means for monitoring and setting the voltage across the motor.
2. (Original) The controller according to claim 1, further comprising an over-current sense circuit for monitoring the current across the electric motor.
3. (Original) The controller according to claim 1, further comprising an over/under voltage sense circuit for monitoring a supply voltage to the electric controller.
4. (Currently Amended) A circuit arrangement in a variable speed electric motor controller, comprising:
 - a controller logic circuit for operating a controller logic finite state machine, wherein the state machine sets the voltage supplied to an electric motor; and
 - a closed loop feedback circuit for generating a signal indicating the voltage across the electric motor, the signal being for comparison to the voltage set by the state machine input to the state machine for monitoring thereof.
5. (Original) The circuit arrangement of claim 4, wherein the state machine comprises at least a running state.

6. (Original) The circuit arrangement of claim 5, further comprising one or more of the following set of states: a sleep state, a lockout state, an overcurrent state, a timeout state, or a battery check state.

7. (Original) The circuit arrangement of claim 4, wherein the controller logic circuit comprises a microprocessor and a memory, each configured for collectively controlling the state machine.

8. (Original) The circuit arrangement of claim 4, additionally comprising a digital to analog converter for converting an 8-bit digital signal to an analog voltage for setting the voltage supplied to the electric motor.

9. (Original) The circuit arrangement of claim 4, wherein the circuit arrangement is directly coupled with the electric motor.

10. (Previously Presented) A system for controlling an electric automotive component comprising a component and the electric motor linear speed controller of claim 1.

11. (Original) The system according to claim 10, wherein the component is an electric motor.

12. (Original) The system according to claim 10, wherein the component is an electric light.

13. (Original) The system according to claim 10, further comprising an over-current sense circuit for monitoring the current across the electric motor.

14. (Original) The system according to claim 10, further comprising an over/under voltage sense circuit for monitoring a supply voltage to the electric controller.

15. (Currently Amended) A system for controlling the speed of an electric motor comprising:

a digital to analog converter means, for converting a digital signal to analog voltage for setting a voltage across said electric motor;

a microprocessor and associated digital memory for generating the digital signal, said microprocessor configured to instantiate and operate a digital state machine for converting the duty cycle of an input signal to the microprocessor and associated digital memory for setting the voltage supplied to the electric motor generated by an associated closed loop feedback means, wherein the state machine sets the voltage supplied to the electric motor based on the input signal; and

a closed loop feedback loop means adapted and configured to monitor, for monitoring the voltage across said motor and generating a signal for input to the microprocessor.

16. (Original) An automobile comprising the system of claim 15.

17. (Original) The automobile of claim 16, wherein the system comprises a temperature-control system.

18. (Currently Amended) A linear speed controller for an automotive electric motor, comprising:

a digital state machine for converting the duty cycle of an input signal for setting the voltage to be supplied to the motor generated by an associated closed loop feedback loop;

an over-current sense circuit, for monitoring the current across said electric motor; an over/under voltage sense circuit, for monitoring a supply voltage to the electric controller;

a digital to analog converter, for converting an 8-bit digital signal to analog voltage for setting voltage across said electric motor; and

a closed loop feedback loop adapted and configured to monitor, for monitoring the voltage across said motor and generating a feedback signal for adjusting the voltage supplied to the motor input to said digital state machine.

19. (New) The electric motor of claim 1 wherein the digital state machine means comprises digital logic means and a state machine, wherein the digital logic means converts the input signal to a digital signal that identifies the duty cycle for input to the state machine.

20. (New) The linear speed controller of claim 18 wherein the duty cycle is provided to the digital state machine by digital circuitry that receives the input signal in pulse-width modulated form in accordance with the duty cycle.